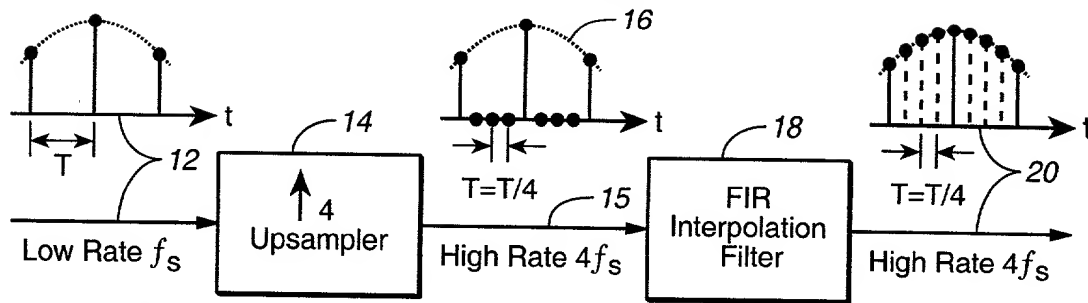
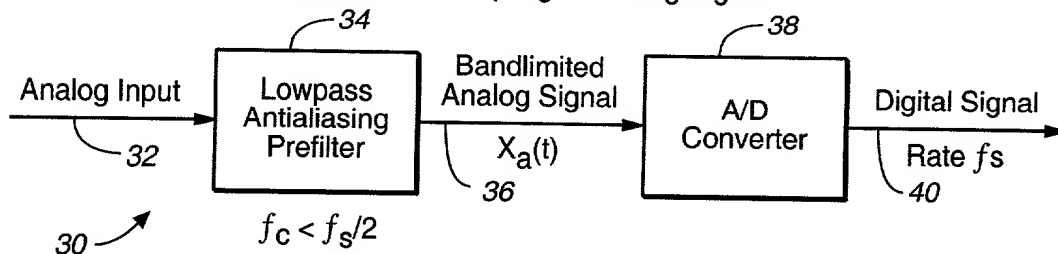
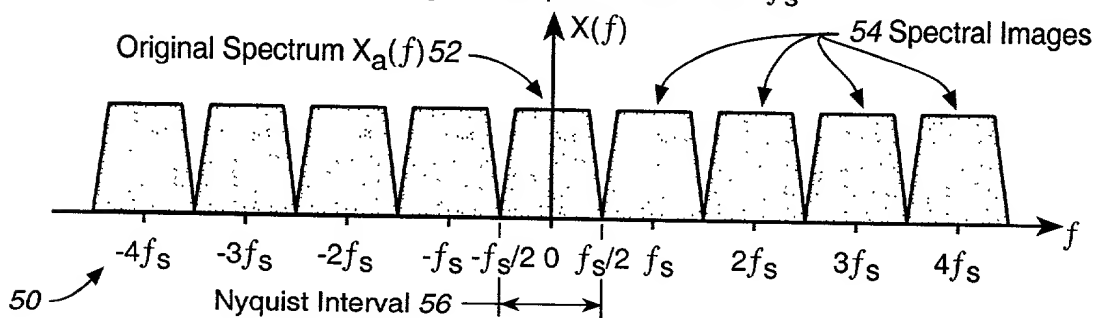


1 / 15

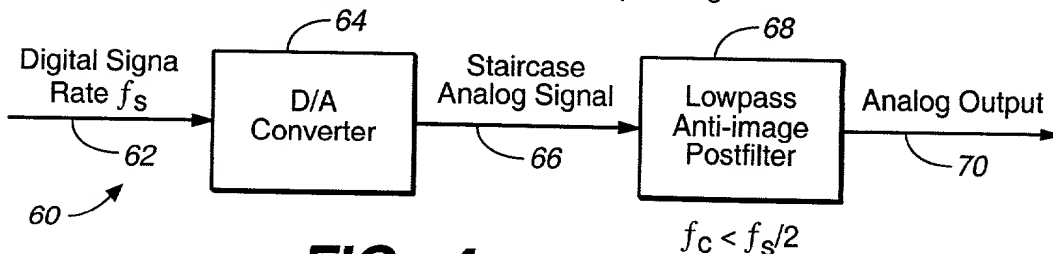
Sampling Rate Increase with Digital Interpolation

**FIG._1 (PRIOR ART)**

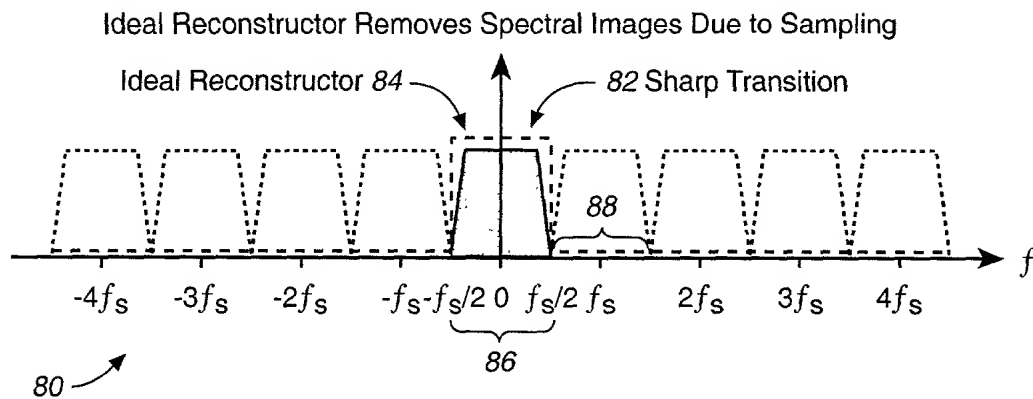
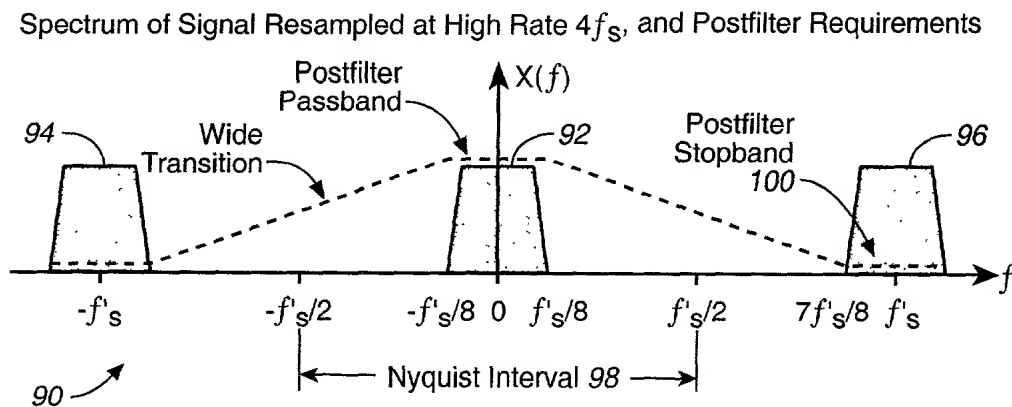
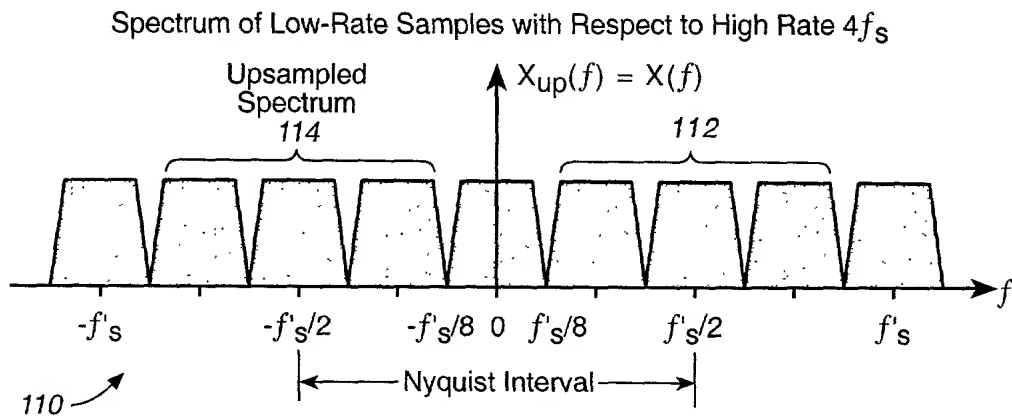
Prefiltering and Sampling of Analog Signal

**FIG._2 (PRIOR ART)**Spectrum of Signal Sampled at Low Rate f_s **FIG._3 (PRIOR ART)**

Analog Reconstruction of Sampled Signal

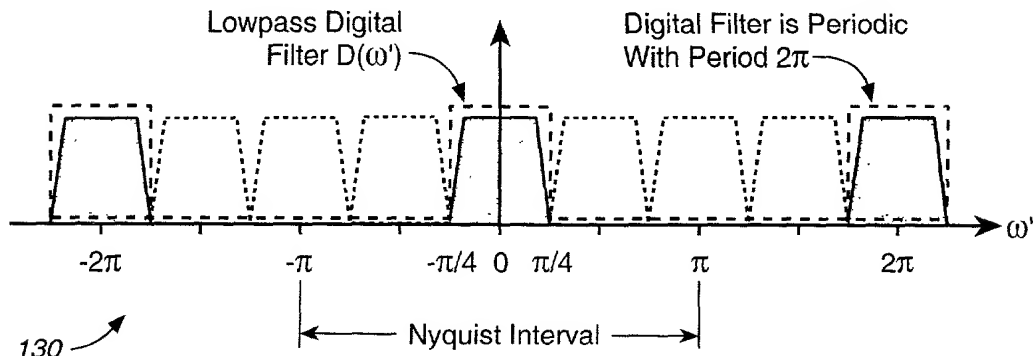
**FIG._4 (PRIOR ART)**

2 / 15

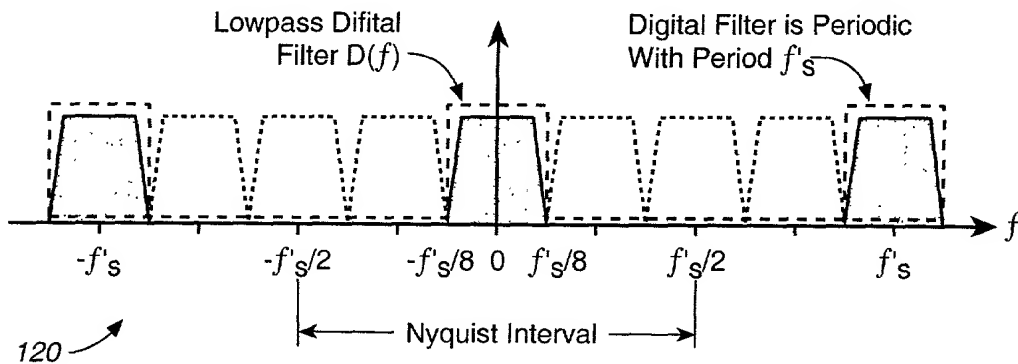
**FIG._5 (PRIOR ART)****FIG._6 (PRIOR ART)****FIG._7 (PRIOR ART)**

3 / 15

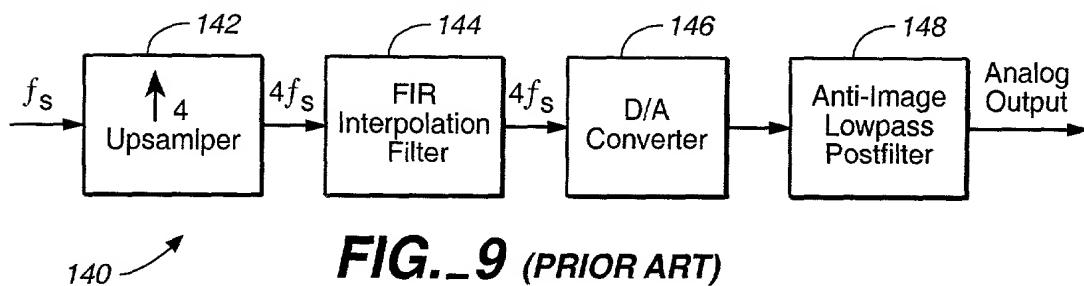
High-Rate FIR Interpolator Removes Intermediate Spectral Images

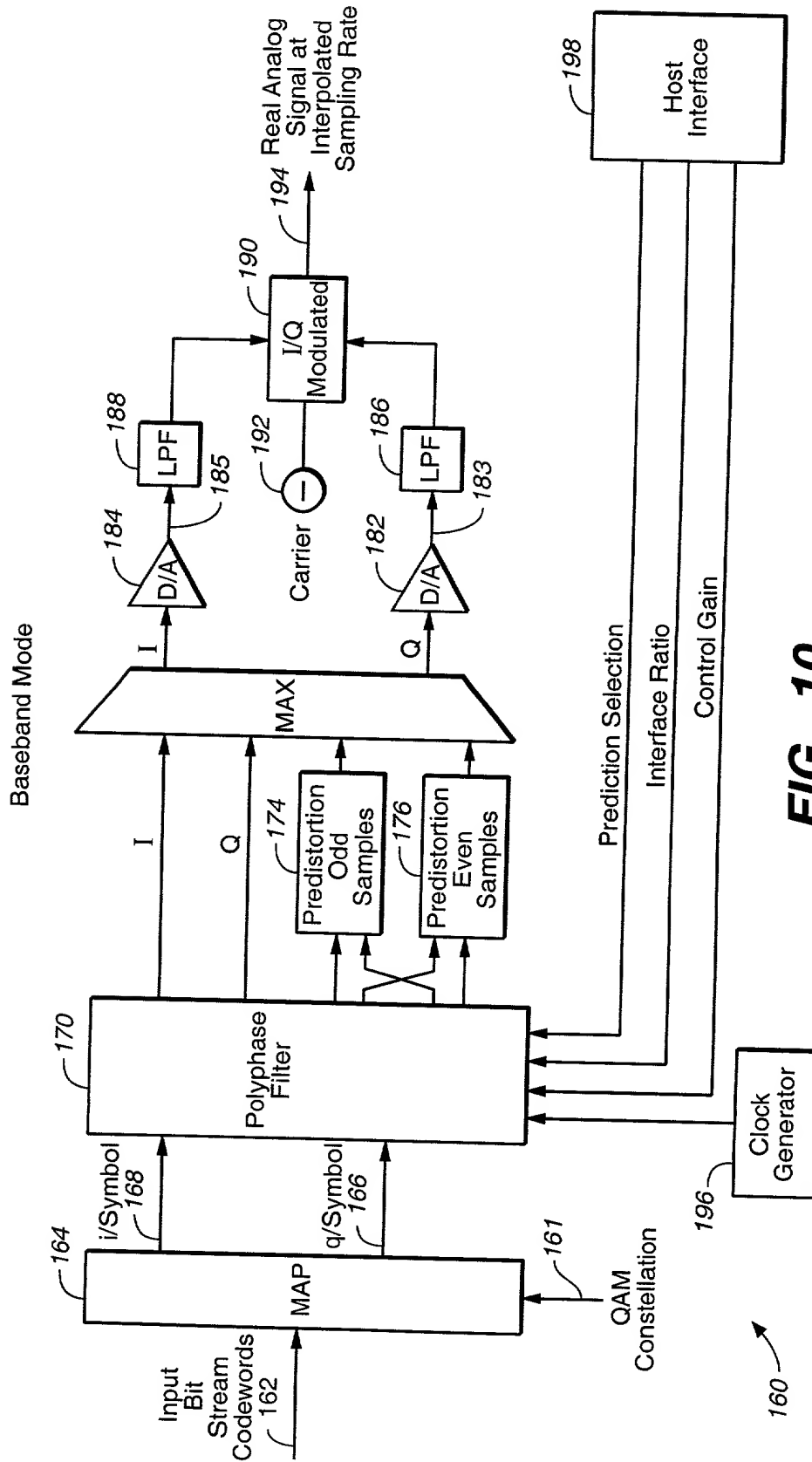
**FIG. 8A (PRIOR ART)**

High-Rate FIR Interpolator Removes Intermediate Spectral Images

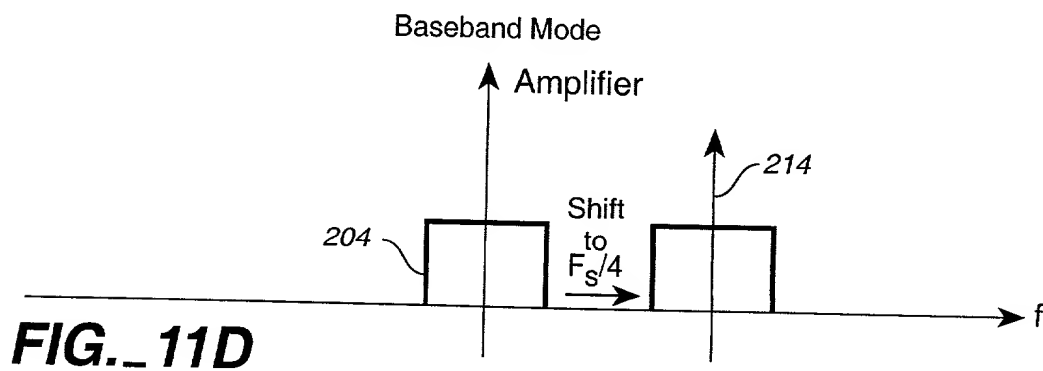
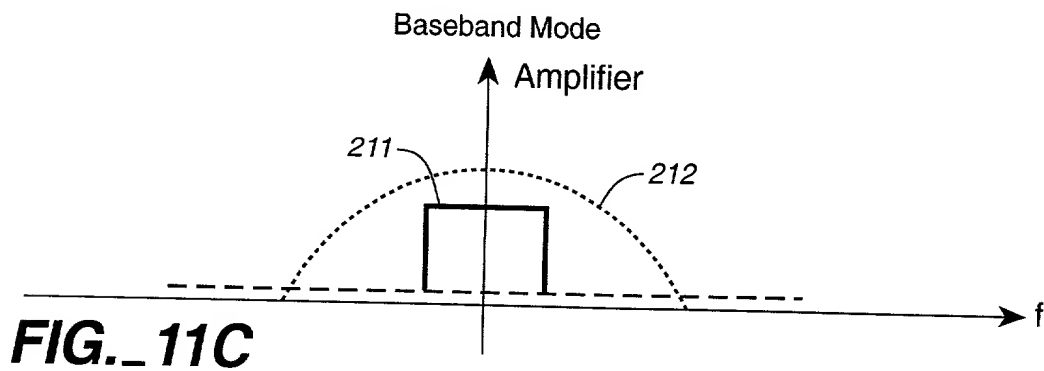
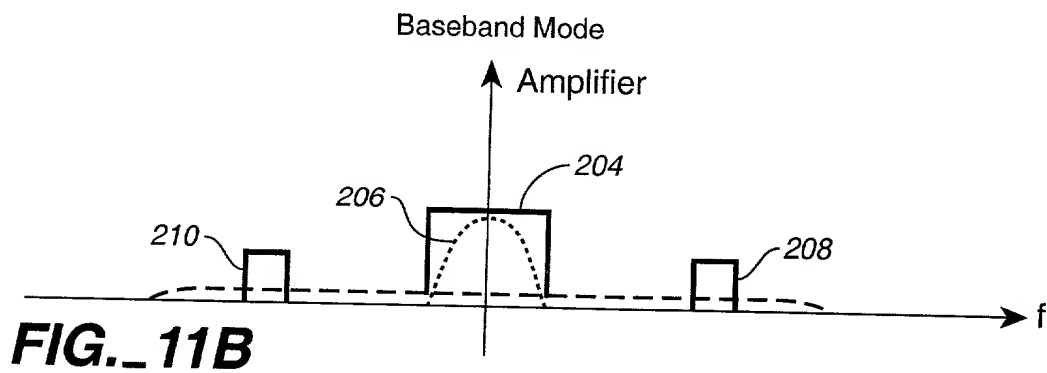
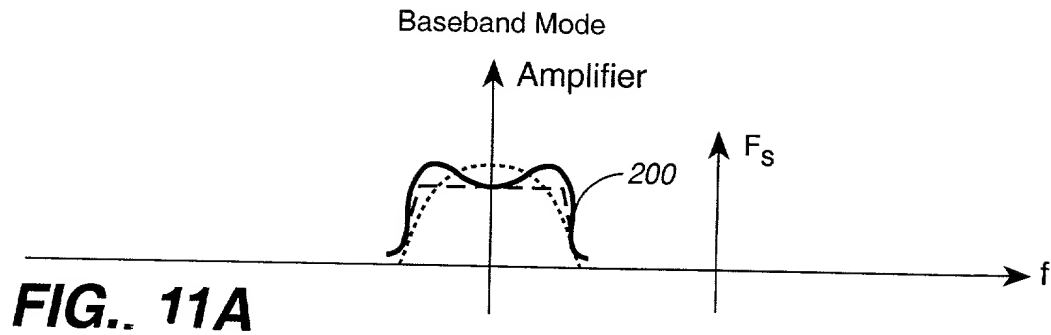
**FIG. 8B (PRIOR ART)**

4-Times Oversampling Digital Filter Helps Analog Reconstruction

**FIG. 9 (PRIOR ART)**

**FIG. 10**

5 / 15



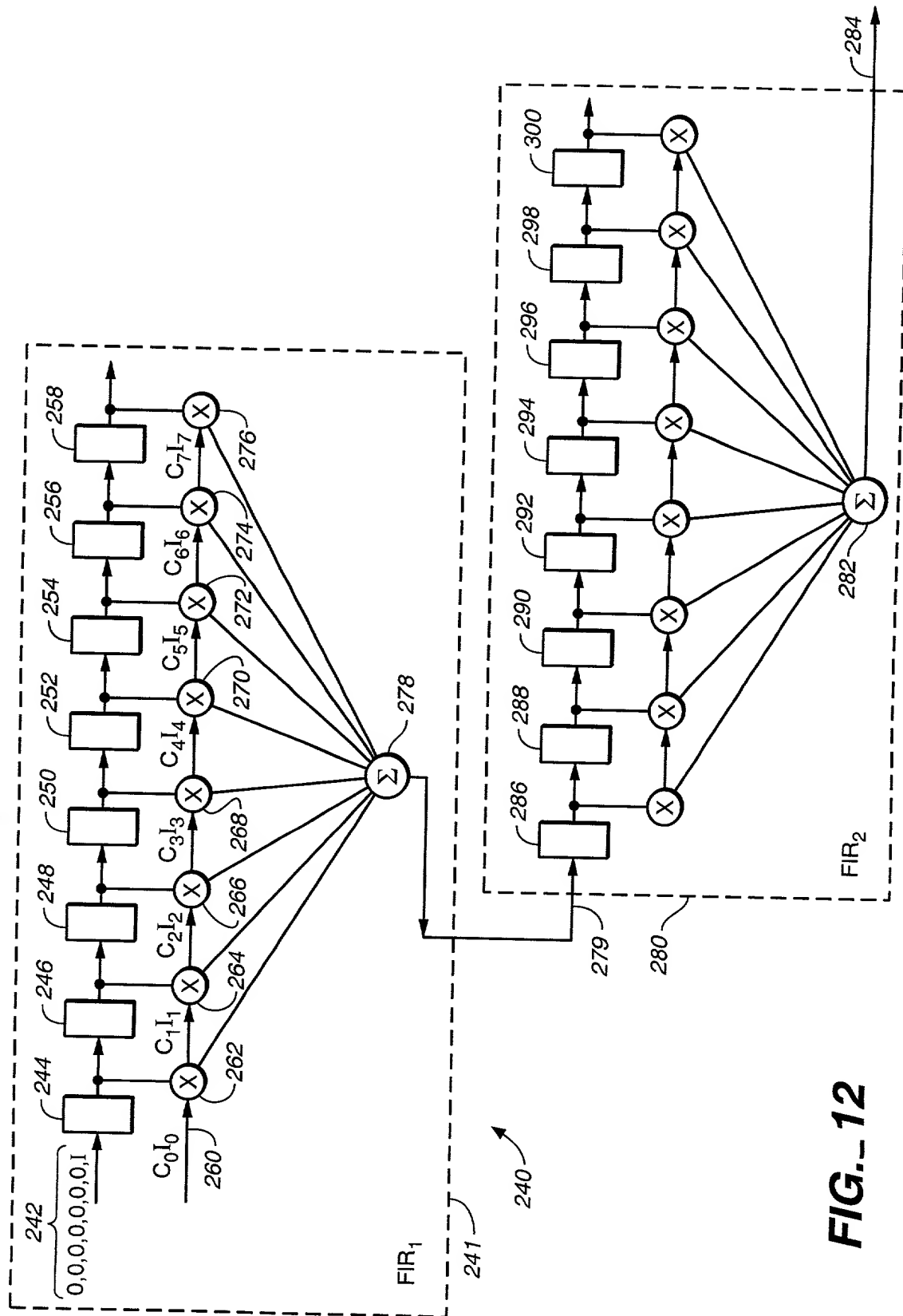


FIG. 12

7/ 15

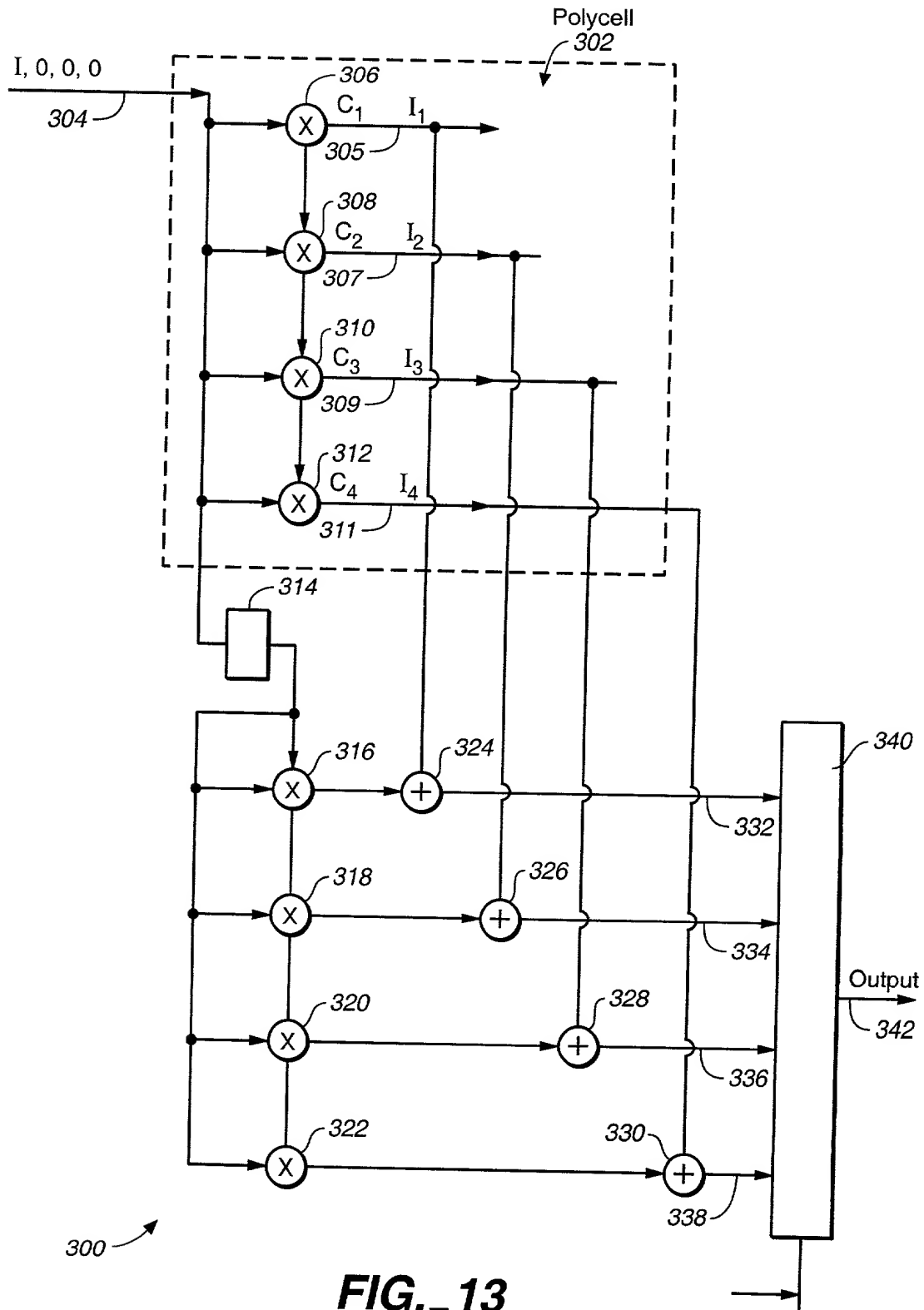
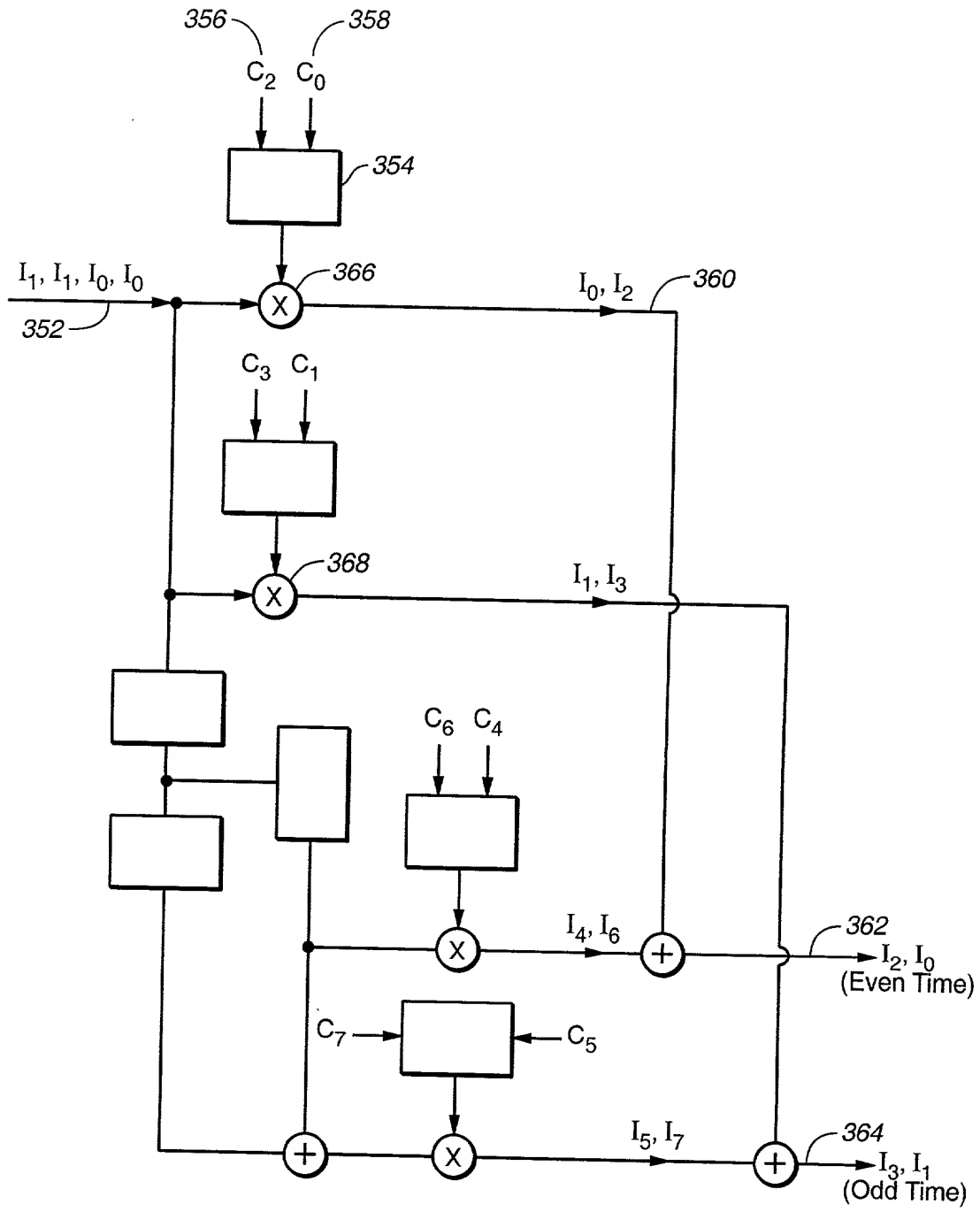


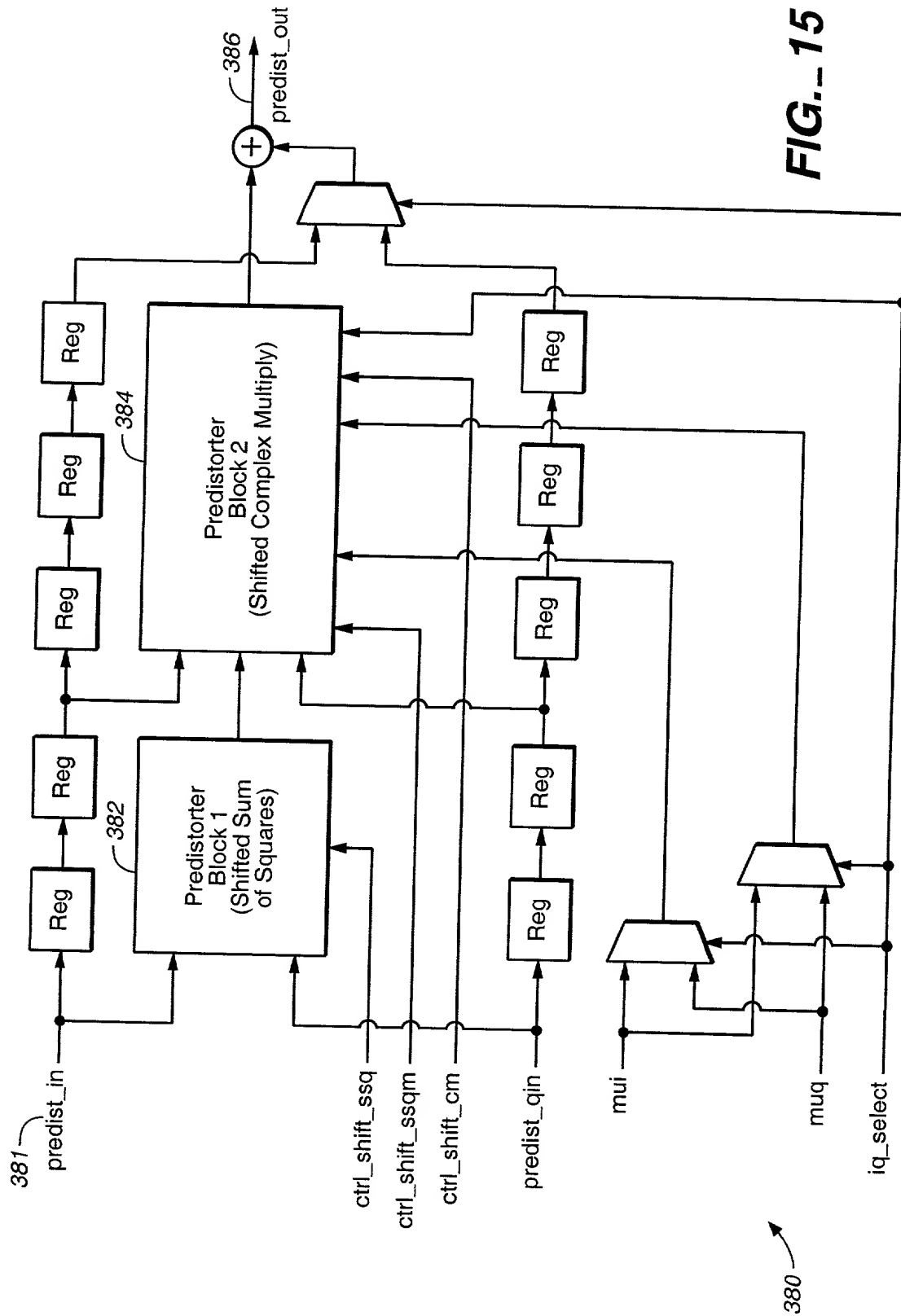
FIG. 13

8 / 15

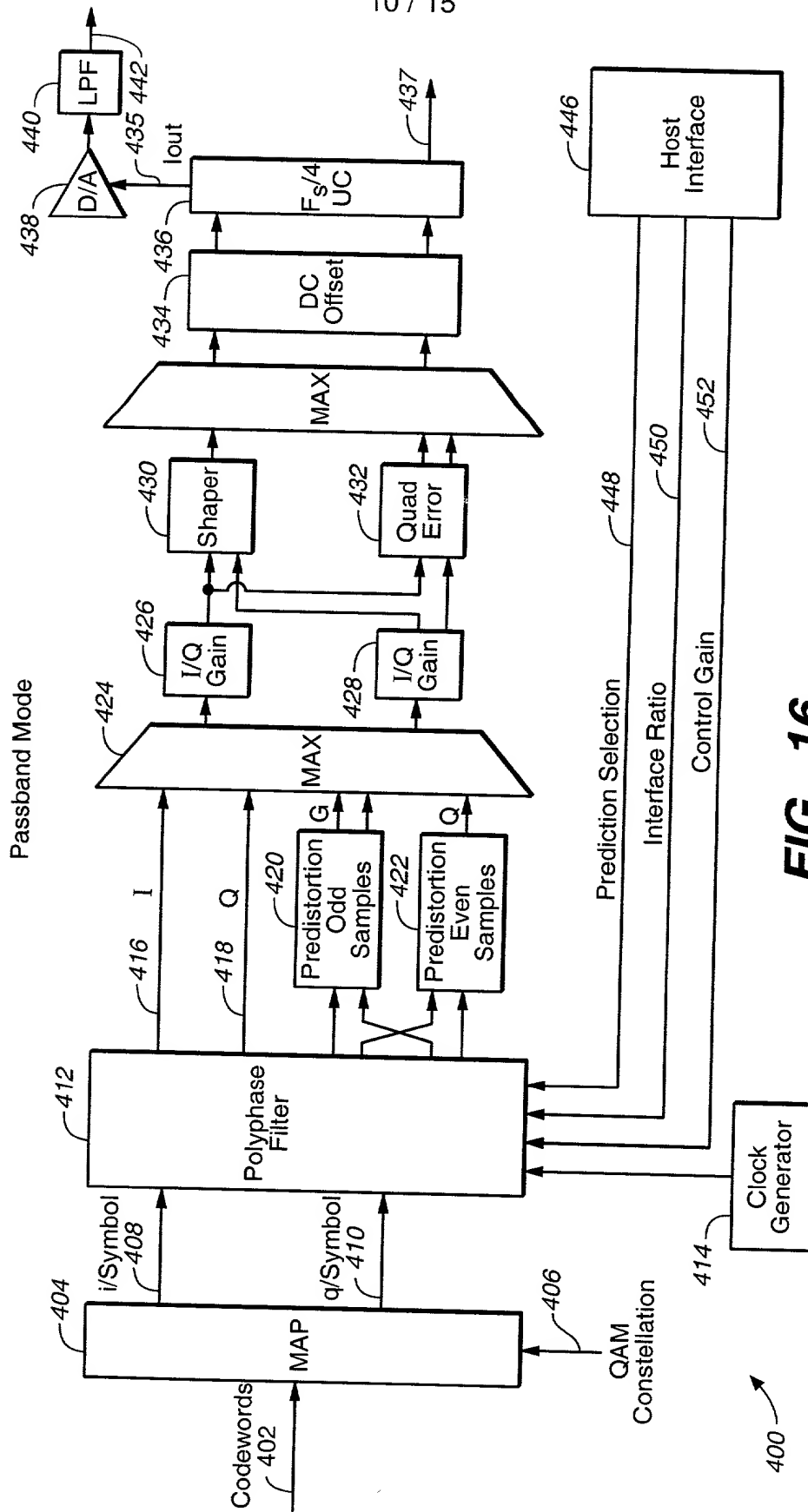


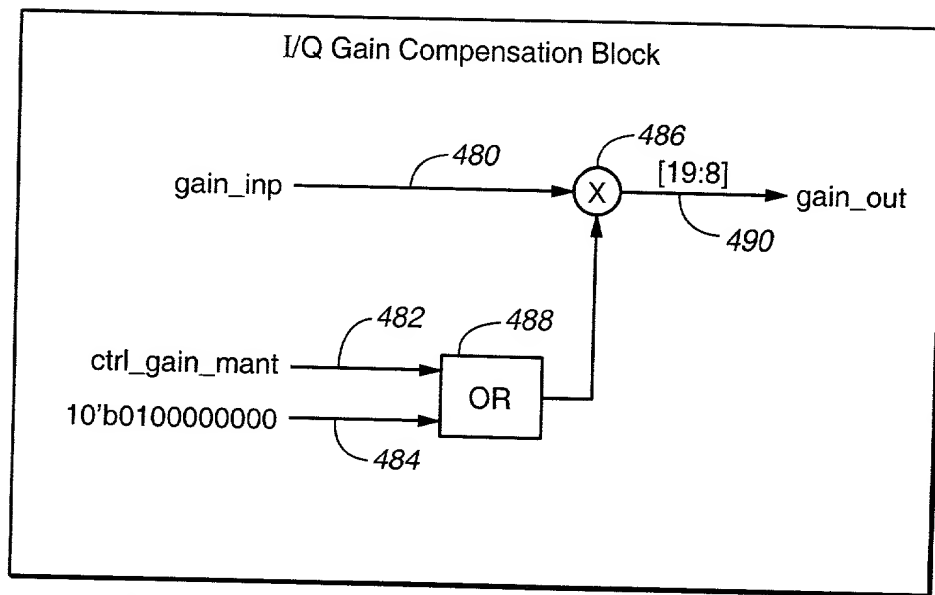
350

FIG. 14

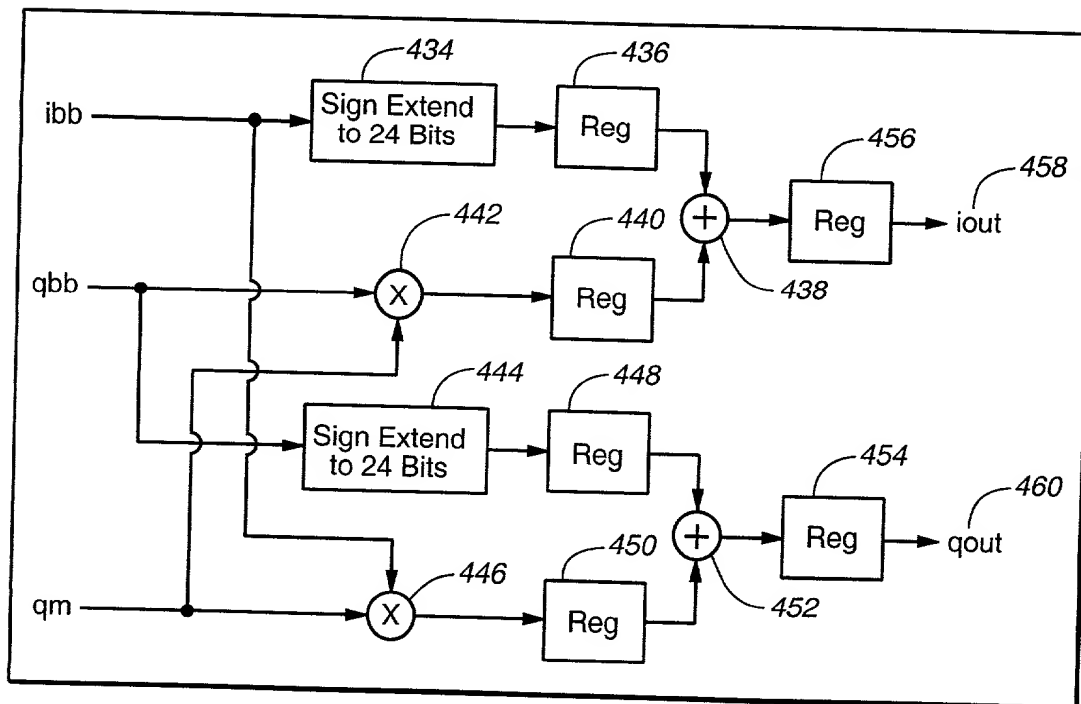


10 / 15

**FIG. 16**



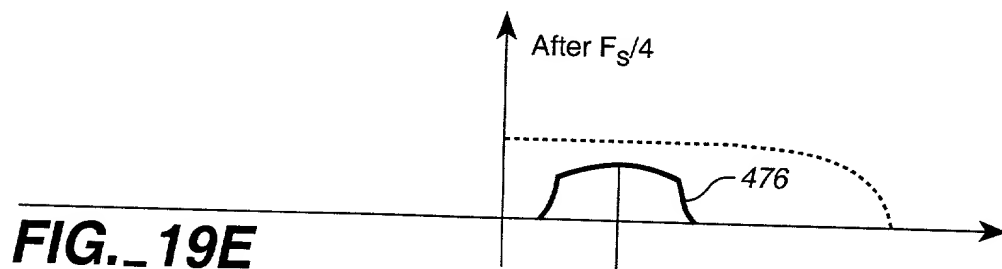
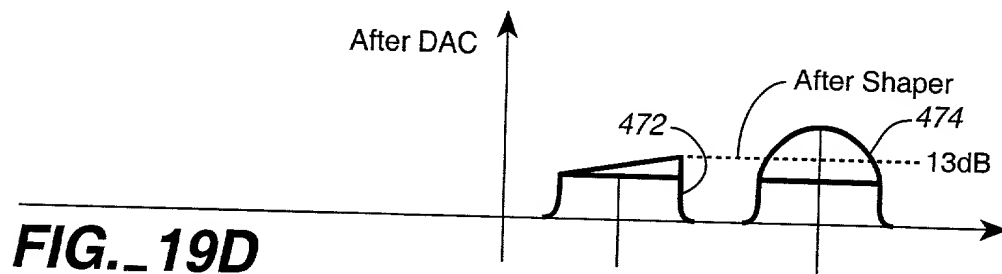
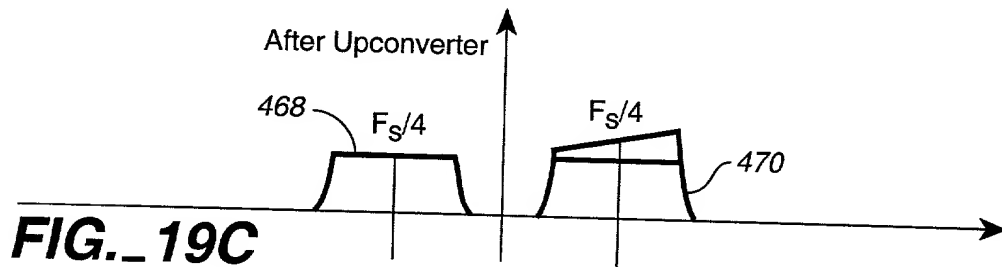
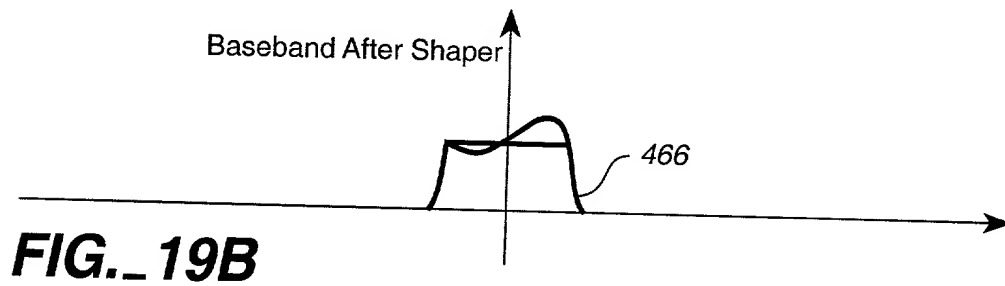
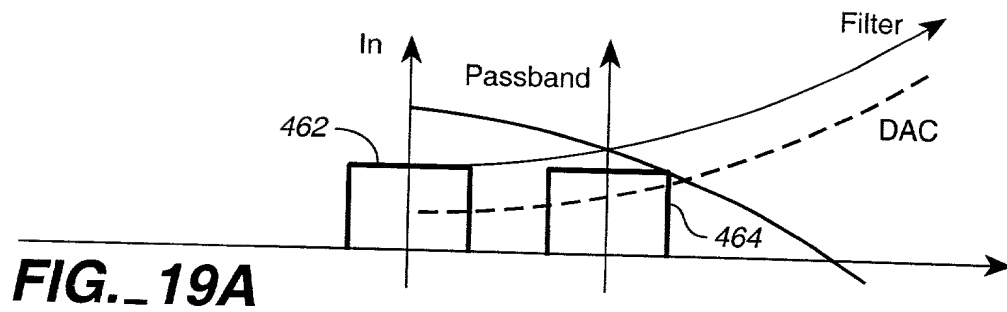
426

FIG. 17

432

FIG. 18

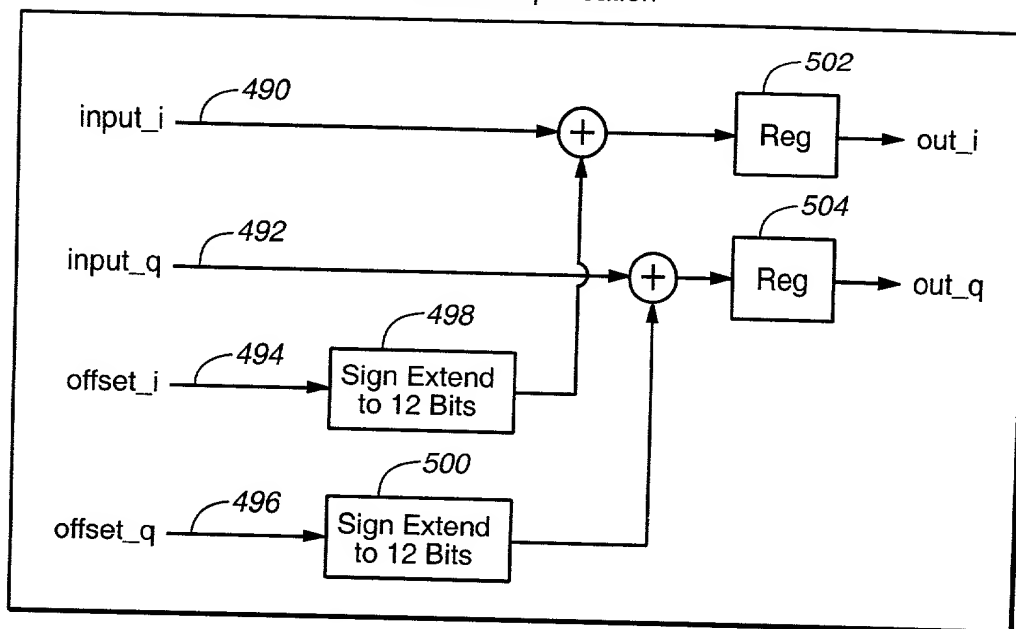
12 / 15





14 / 15

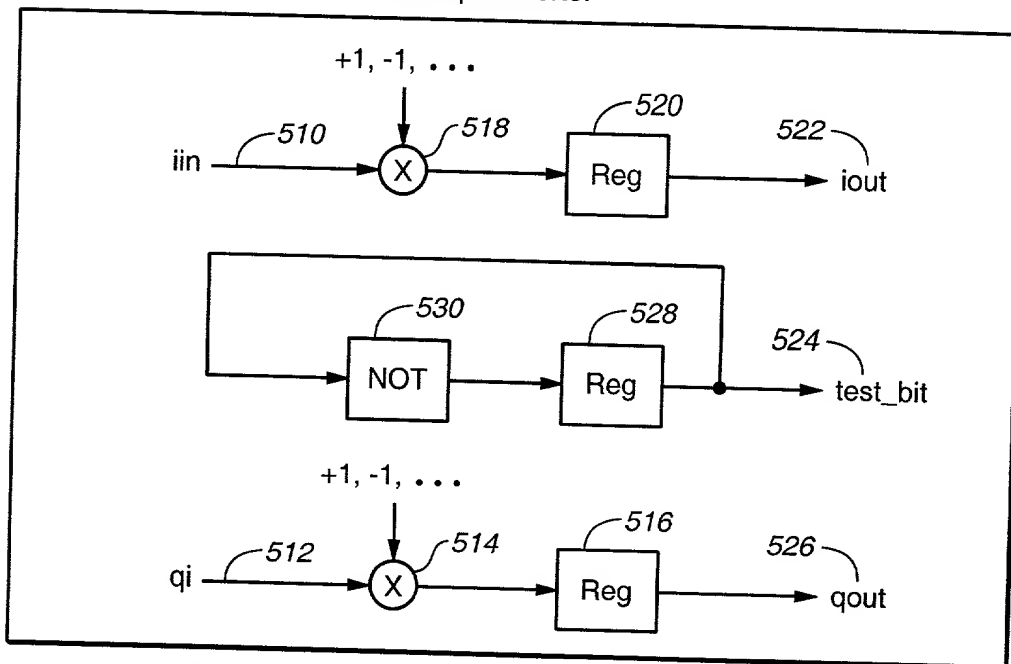
DC Offset Compensation



434

FIG. 21

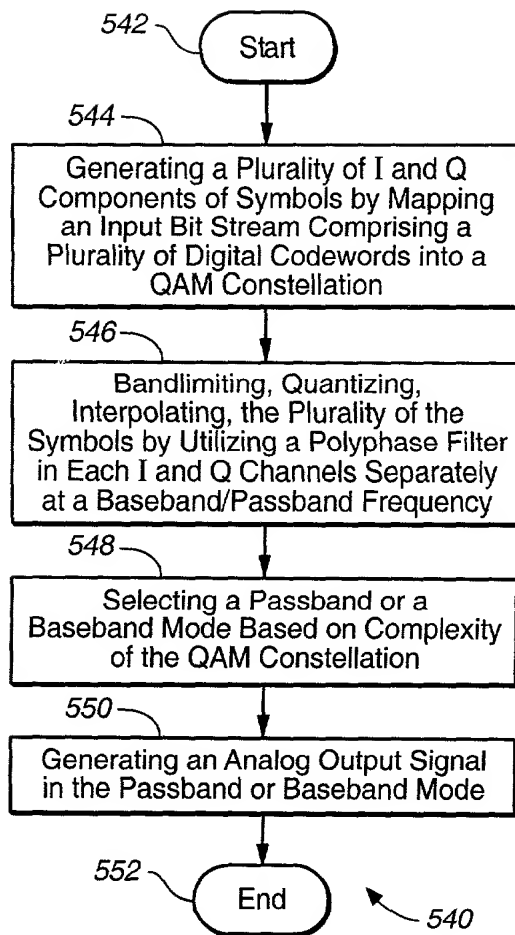
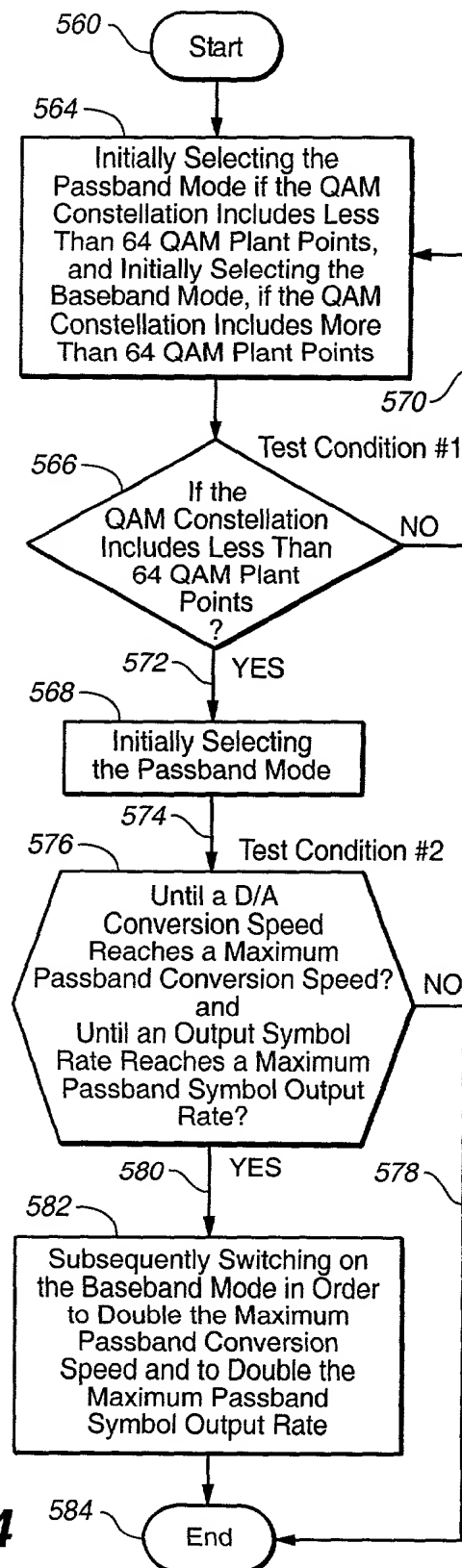
Fs/4 Upconverter



436

FIG. 22

15 / 15

**FIG. 23****FIG. 24**